Question			Answer	Mark
1(a)(i)	Light emitting diode OR LED			B1
(a)(ii)	->-			В1
(b)	column C 0 0 0 0 0 0 0 1	column E 0 1 0 1 0 1 1 0 1 1		ВЗ
(c)	Replace the OR gate w	rith an AND gate		B1
				Total: 6

2 (a) AND (gate) В1 **(b)** 001 B2 100 010 110 (c) F Α В 1 B3 [Total: 6] (a output of A: 1, 1, 0, 0 c.a.o. [1] output of B: 0, 1, 0, 0 e.c.f. from candidate's output of A [1] (b) dark AND hot owtte note: must be consistent with answer to (a) [1]

[2]

(c) B cannot provide enough power / current for lamp, or equivalent

note: statement of function of a relay without reference to context gains 1 mark

OR allows remote lamp

- 4 (a) (i) OR (gate)

 (ii) 1 input and 1 output labelled with words

 (iii) correct symbol

 (b) (needle not deflected

 (ii) needle not deflected

 B1

 (iii) needle deflected either way

 B1
- 5 **(a (i)** NAND B1
 - (ii) output and one input correctly labelled B1
 - (b) rectangle with longitudinal line in middle third, no input or output wire required B1
 - (c) (i) temperature (decreases)
 - (ii) correctly relates change of resistance to change of temperature voltage of mid-point (of potential divider)/left of LED increases OR higher V across thermistor

 current flows through/enough V to light LED

 B1
 - (d) $1/R_p = 1/R_1 + 1/R_2 \text{ or } (R_p) = R_1R_2/(R_1 + R_2)$ (R = $1/(1/4 1/6) = 12 \Omega$ A1

[Total: 9]

[Total: 6]

6 **(a)** row 1 0 accept low/off В1 row 2 0 1 accept low/off and high/on В1 row 3 1 1 accept high/on В1 (b) 2 wires to flat (input) side, 1 wire from curved (output) side do not accept pointed curved side or small circle B1 (c) NOT gate connected to output of AND gate accept labelled boxes for gates do not allow any extra gates or inputs M1 NOT gate correct way round Α1 [Total: 6] **B**1 7 (a) in order downwards: 1 1 1 0 c.a.o. (b) 1 AND 0 (e.c.f. from **(b)(i)**) В **B**1 (ii) NOT (gate) (allow NOR (gate)) В (c) R = 1 AND S = 0 (e.c.f. from (b)(i))T = 1B1 [5]

8	(a)	(i)	AND gate	B1	
		(ii)	correct symbol must have 2 inputs, 1 output concave input side, somewhat pointed on output side with small circle	B1	[2]
	(b)	(HIGH/1	В	
		(ii)	HIGH/1	В	[2]
	(c)	tra	nsistor circled	B1	[1]
				[Tota	1. 51